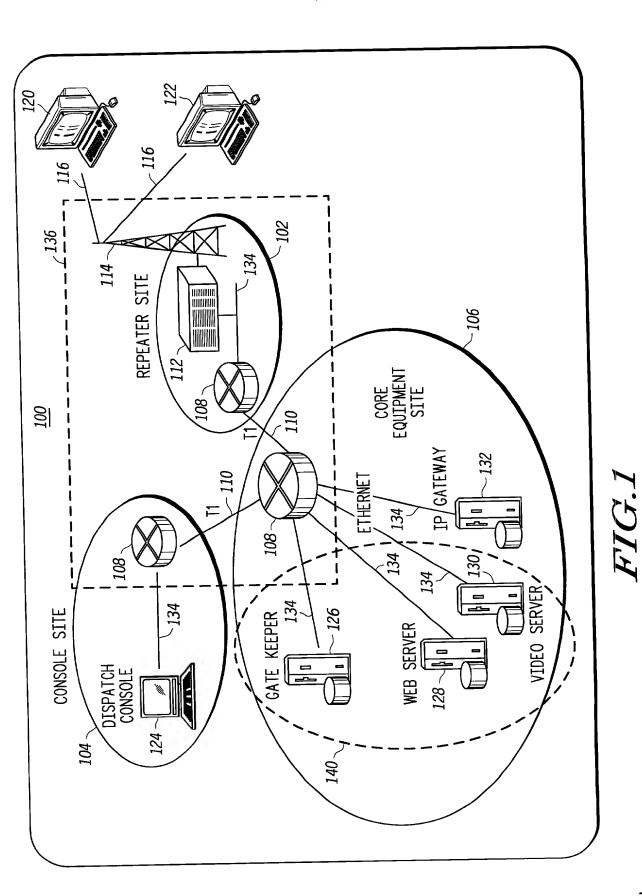


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2/8

SLOT HEADER <u>205</u>

> DATA BLOCK <u>210</u>

DATA BLOCK <u>210</u>

<u>200</u>

MAC HEADER 305

LINK LAYER HEADER 310

LINK LAYER HEADER 310

CRC 320

<u> 205</u>

FIG.2

FIG.3

SLOT TYPE <u>405</u>	RES <u>425</u>
NEXT SLOT COMMUNIC UNIT ID NUMBE	
MAC DESTINATI ID NUMBER	ON <u>415</u>
MAC SOURCE ID NUMBER	<u>420</u>

<u>305</u>

SLOT TYPE RES 505 525

MAC DESTINATION ID NUMBER 515

MAC SOURCE ID NUMBER 520

<u>305</u>

FIG.4

FIG.5

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ACKNOWLEDGEMENT 605 ID NUMBER **ACKNOWLEDGEMENT** PACKET NUMBER 610 615 **ACKNOWLEDGEMENT ACK RESERVED** BLOCK NUMBER BIT 630 625 **FEC** CONFIRM **RESERVED** 630 BIT 622 620 PACKET NUMBER 635 TOTAL NUMBER **BLOCK** OF BLOCKS NUMBER 640 645 LAST BLOCK LENGTH *650*

720 CONFIRM **FEC** RESERVED 730 BIT *725* PACKET NUMBER 735 TOTAL NUMBER **BLOCK** NUMBER OF BLOCKS 745 740 LAST BLOCK LENGTH 750

310

FIG.7

FIG.6

310

<u>210</u>

TRANSMITTED DATA 805

FIG.8

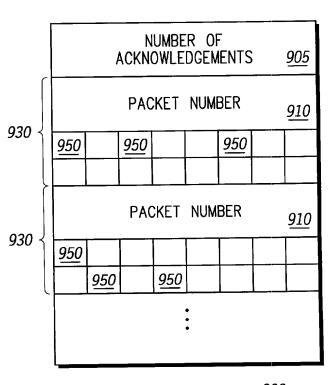
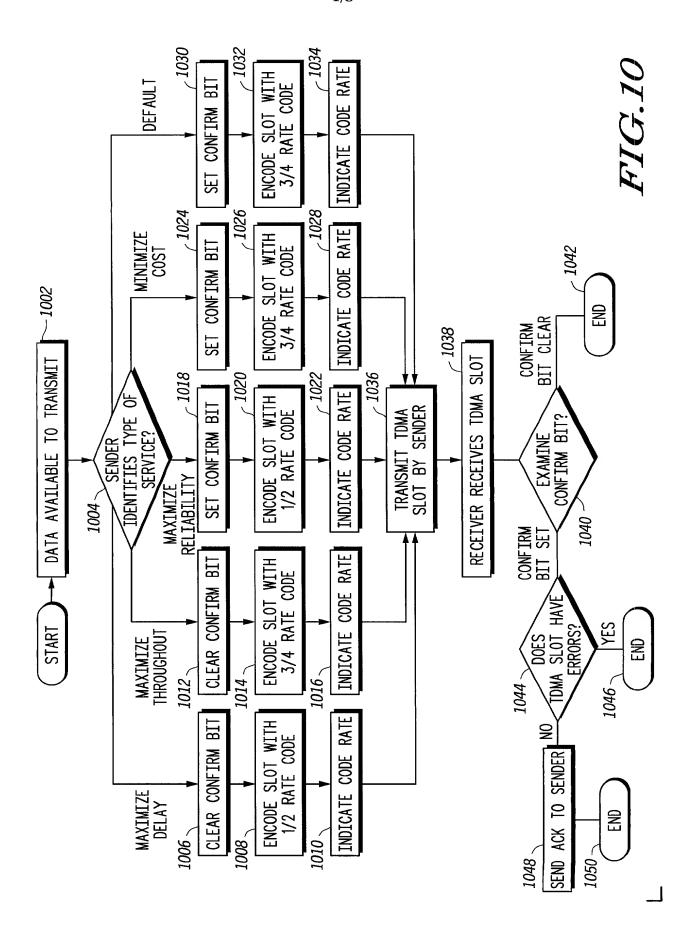


FIG.9

<u>900</u>



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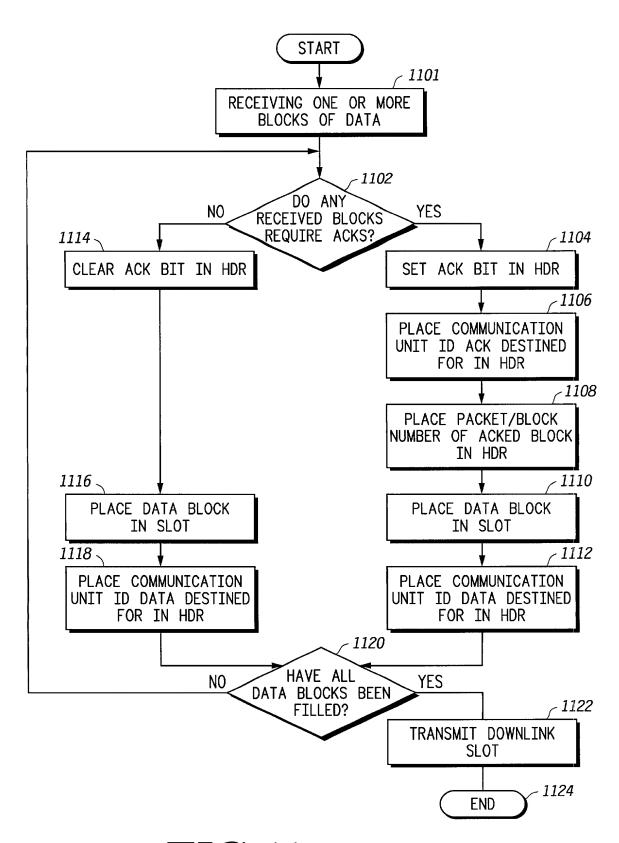


FIG.11

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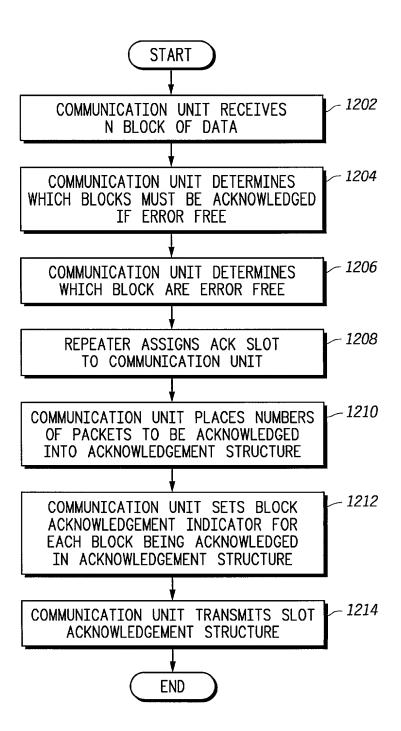


FIG.12

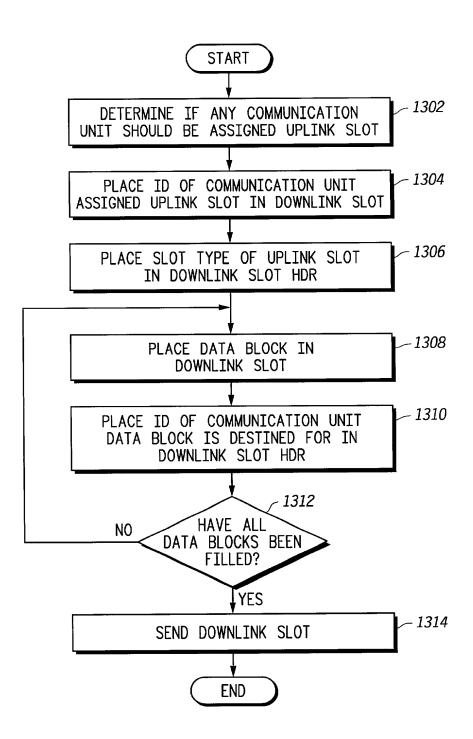


FIG.13

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8/8

MAC HEADER	<u>1405</u>
CRC	<u>1430</u>
LINK LAYER HEADER	<u>1410</u>
DATA BLOCK	<u>1420</u>
LINK LAYER HEADER	<u>1412</u>
DATA BLOCK	<u>1422</u>

1400

FIG.14

